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Form PTO-1 (REV. 8-83)	OS Dept. of Conf.	US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 038839.02		APPLICATION NO. Rule 53(b) Divisional of					
INFORMATION DISCLOSURE STATEMENT					App	lication No.	09/400,303				
(Use several sheets if necessary)			APPLICANT(S) Mitsutoshi MIYASAKA								
			Mitsutoshi MIYASAKA								
			FILING DATE March 19, 2002		GROUP 20						
EVALORE		U.S. PATE	NT DOC	UMENTS			O D				
EXAMINE INITIAL			DATE	NAME		CLASS	SUB CLASS				
(A)	5,858,819	01/1	999	Miyasaka			/				
	5,827,773	05/1	997	Voutsas							
	6,066,516	5/20	00	Miyasaka			/-				
	6,017,719	1/20	1/2000 Yiyasaka				/				
	5,904,770	5/19	99	Ohtani et al.			/				
	6,074,901	6/20	00	Ohtani et al.							
	6,121,660	9/20	00	Yamazaki et al.							
\	6,113,689	9/200	00	Moon							
-	FOR	EIGN PA	TENT DO	CUMENTS							
	DOCUMENT NUMBER		ATE	COUNTRY		CLASS	SUB CLASS				
12	JP-A-4-298020	10/19	92	Japan							
	JP-A-4-100210	4/199	2	Japan							
	JP-A-8-107067	4/199	6	Japan							
	5-129332	5/199	3	Japan							
	3-292719	12/19	91	Japan			/				
	3-178124	8/199	1	Japan		/					
	3-72617	3/199	1	Japan		/					
	62-104021	5/198	7	Japan	-	_/					
	6-216044	8/199	4	Japan		7					
-V	63-151014	6/1988	3	Japan		/ +					
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				tle, Date, Pertinent Pages, etc.)							
LP	Levinson, J., et al., "Conductivity Behavior in Polycrystalline Semiconductor Thin Film Transistors," J. Appl. Phys. 53 (2), February 1982, pp. 1193-1202.										
DATE CONSIDERED											
Examiner: I	nitial if citation considered, whether or not conformance and not considered. Include conv	citation is	in confo	rmance with M.P.E.P. 609; draw	line utr	ugh citation	ı if not in				
	conformance and not considered. Include copy of this form with next communication to applicant.										

Date: March 19, 2002